

Fig. 1a

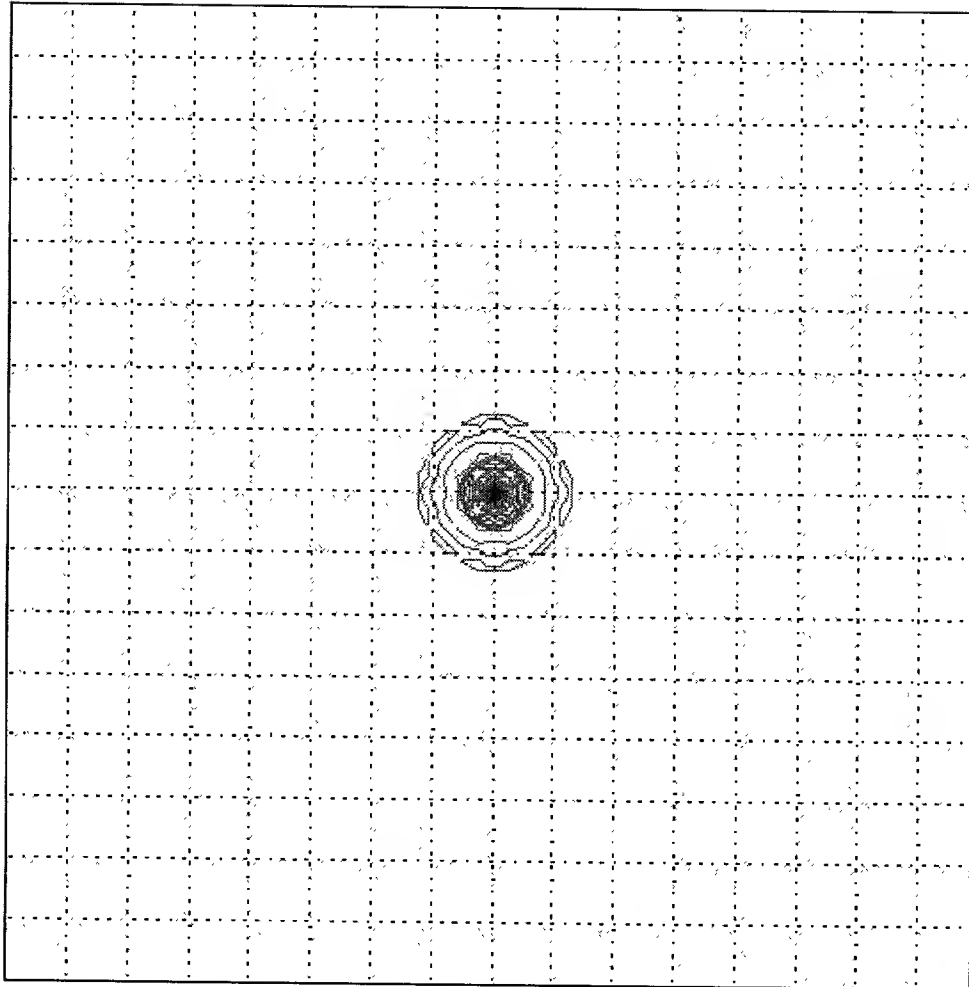


FIG. 1a

Fig. 1b

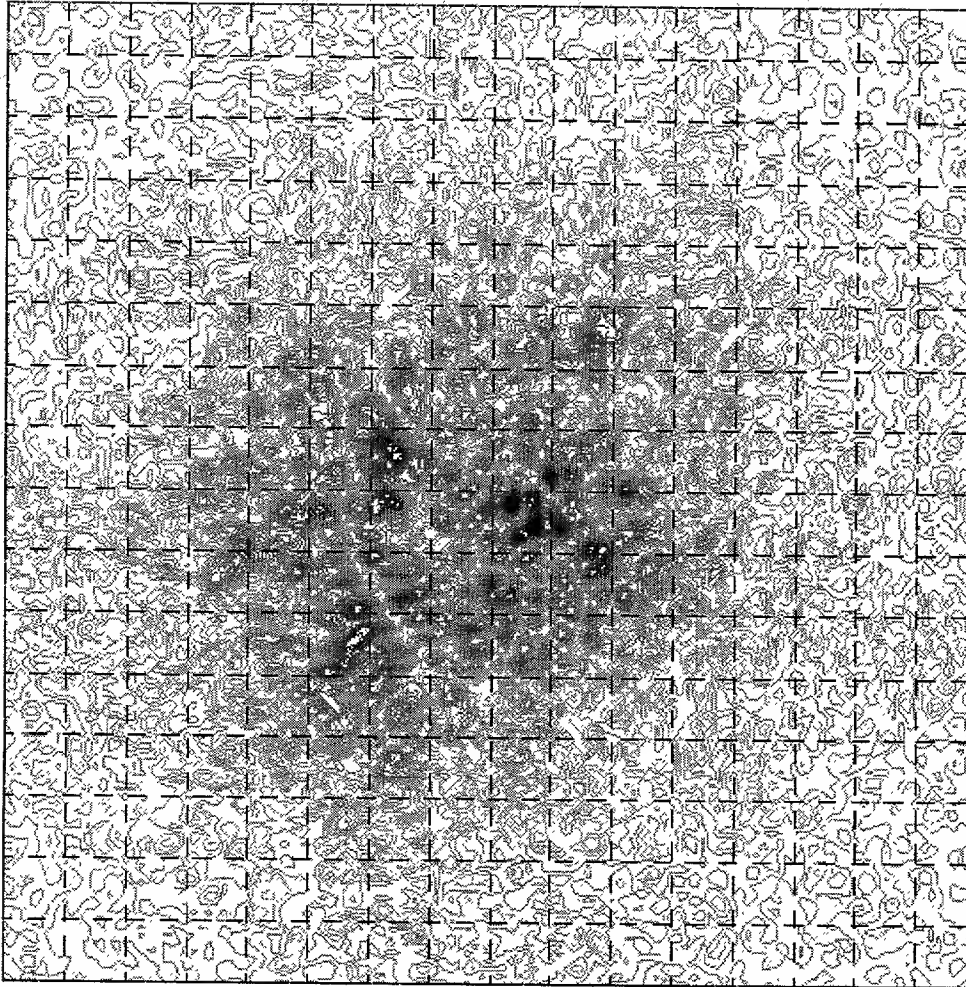


FIG. 1b

Fig. 2

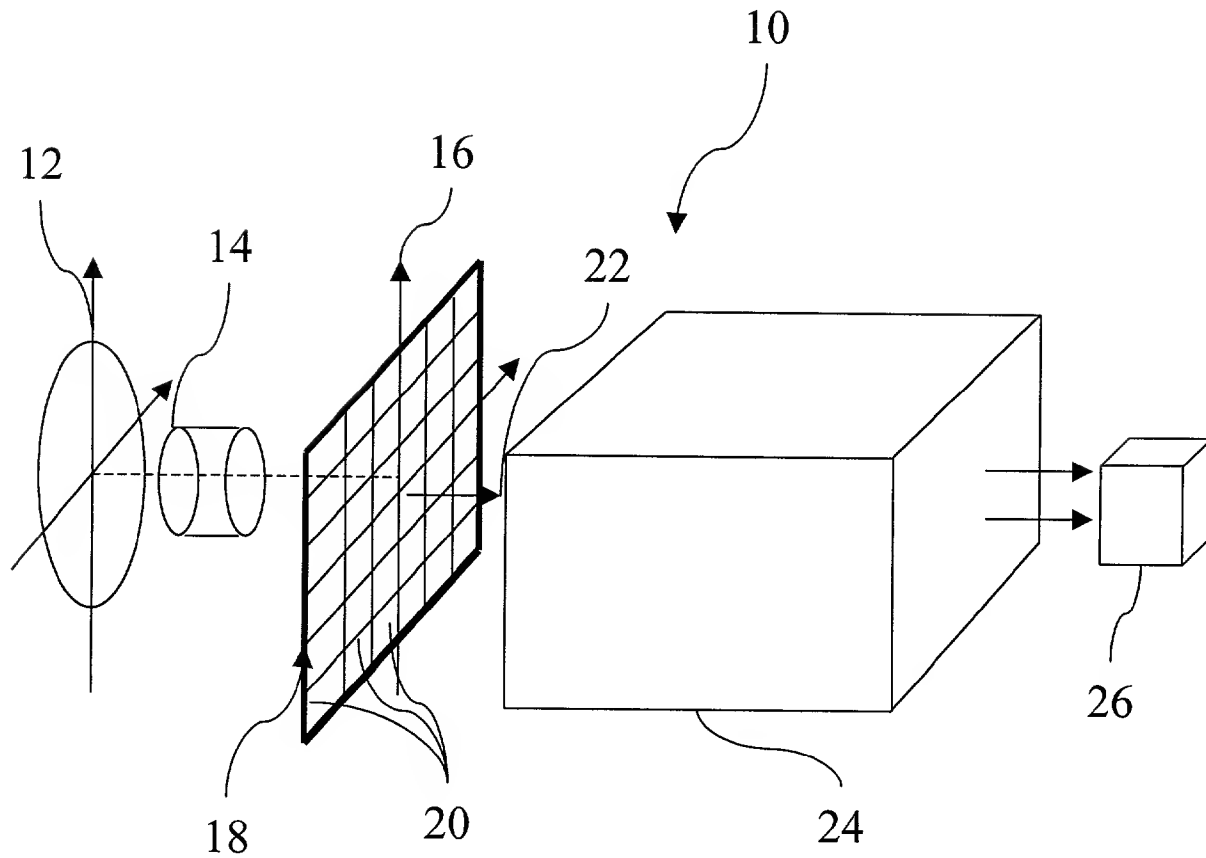
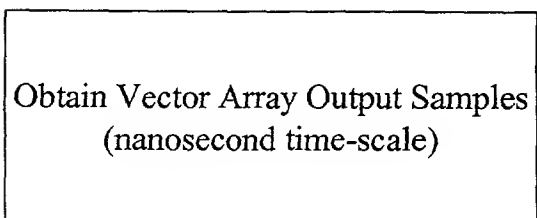
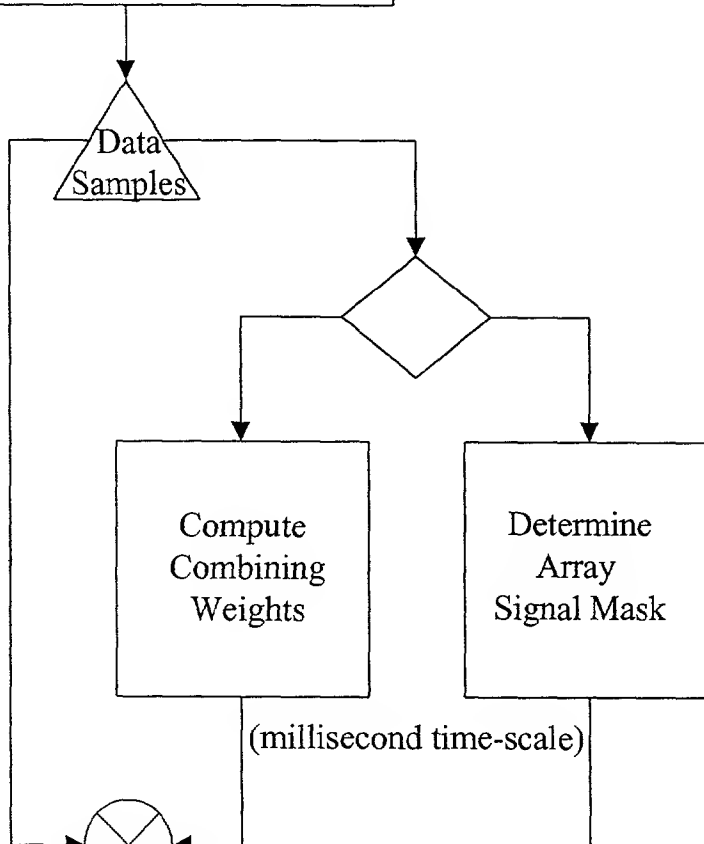


Fig. 3

Step 1

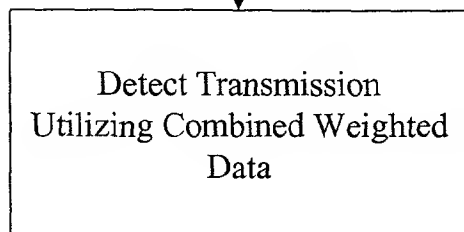


Step 2



Step 3

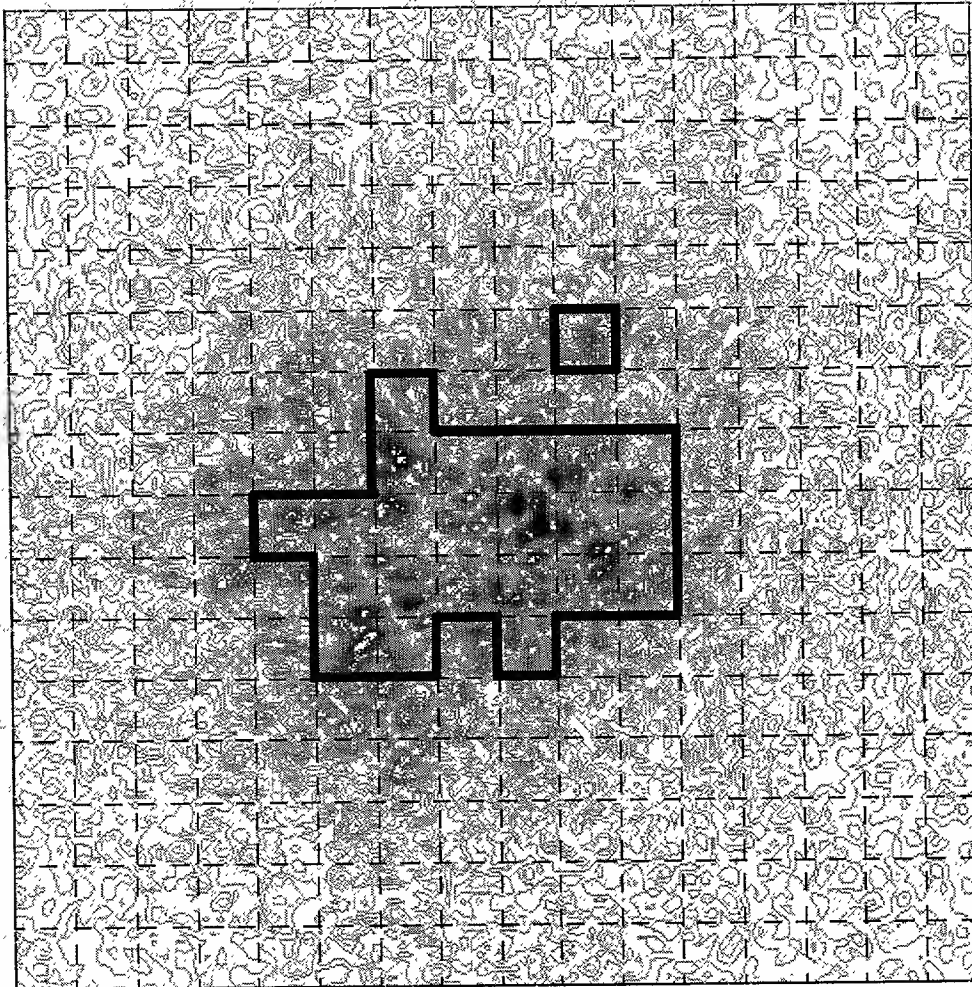
Step 4



Step 5

Signal Output

Fig. 4



47456/JWP/C766

Fig. 5

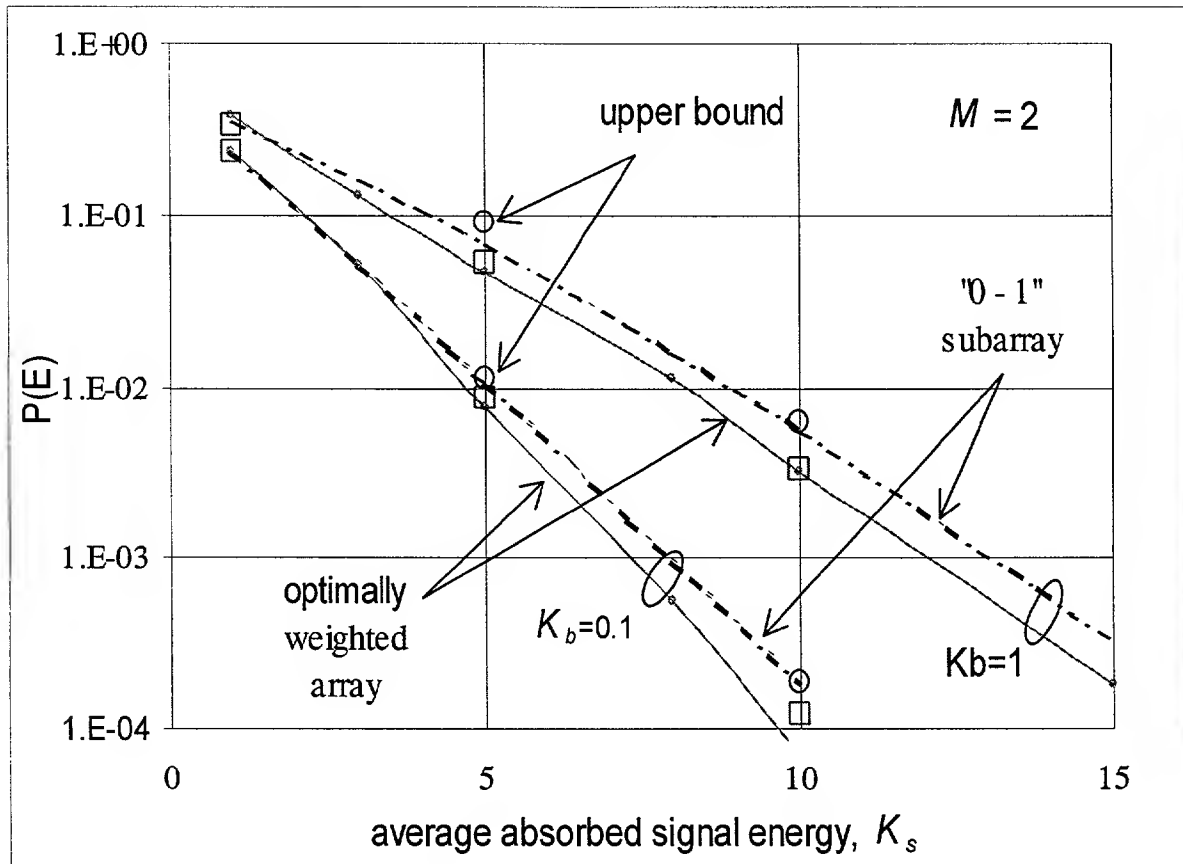


Fig. 6

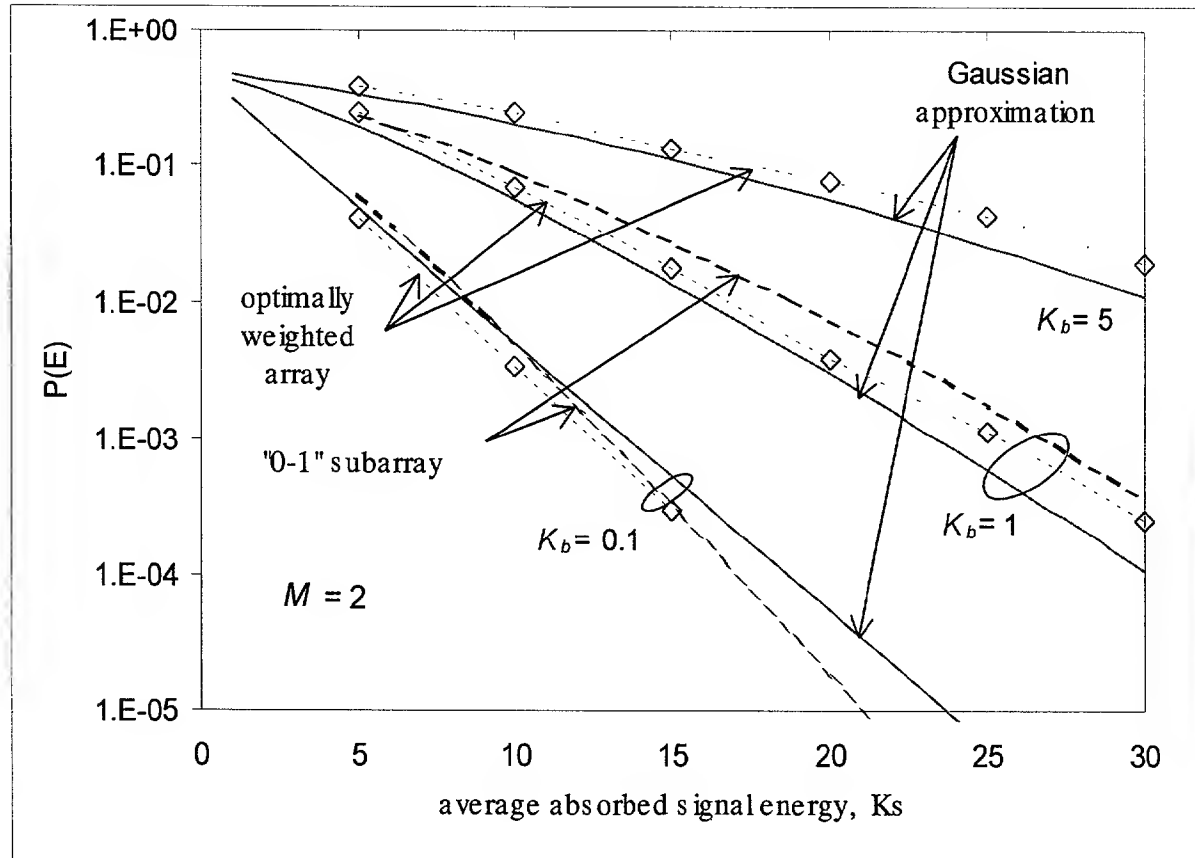
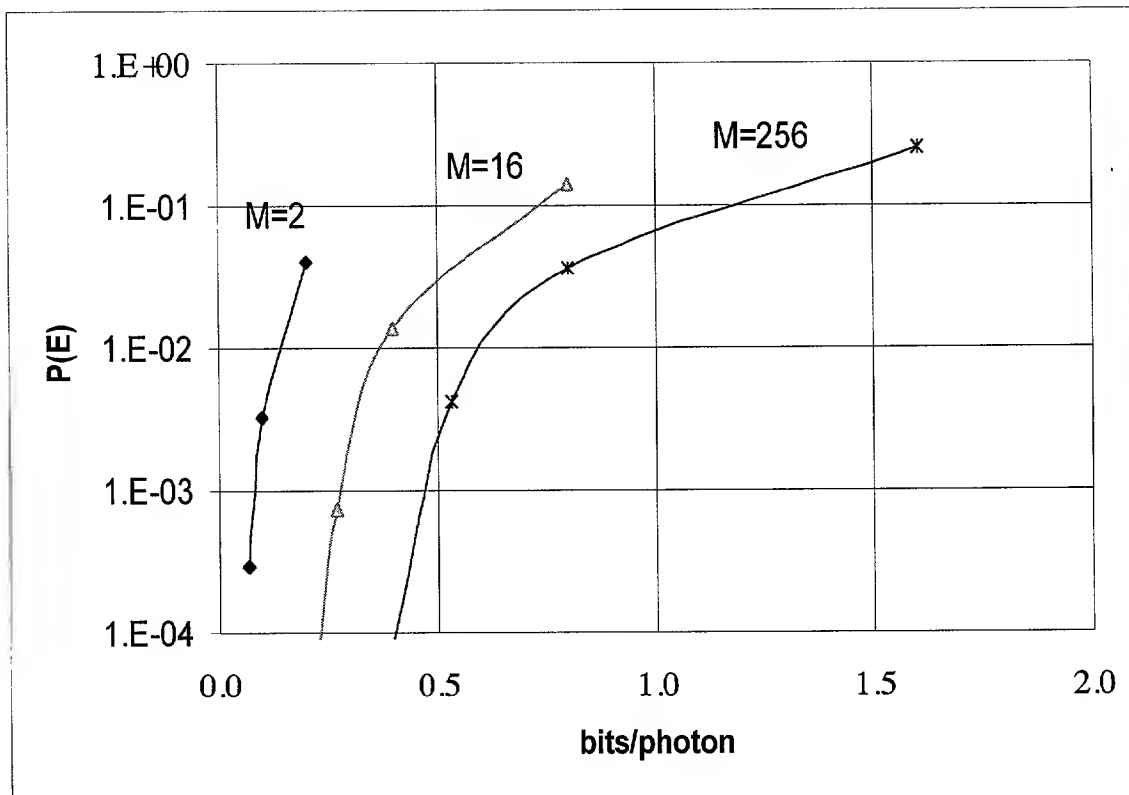
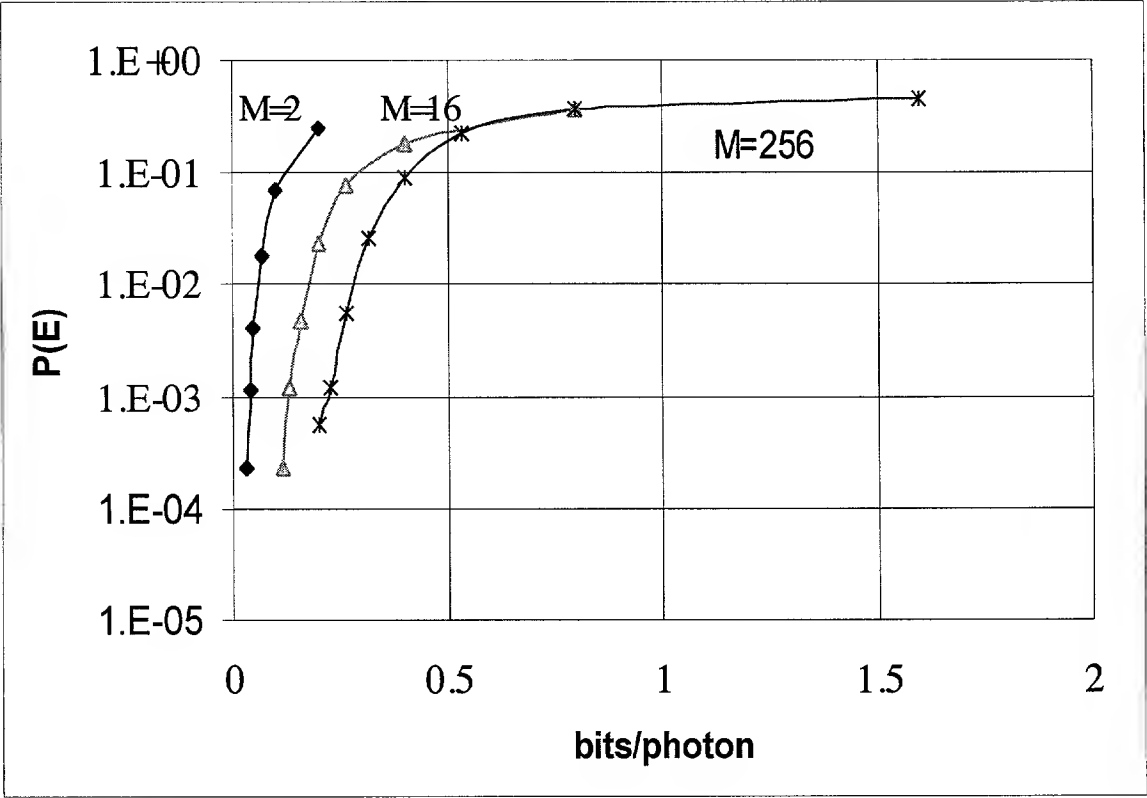


Fig. 7a



$$K_b = 0.1$$

Fig. 7b



$K_b = 1.0$

Fig. 8

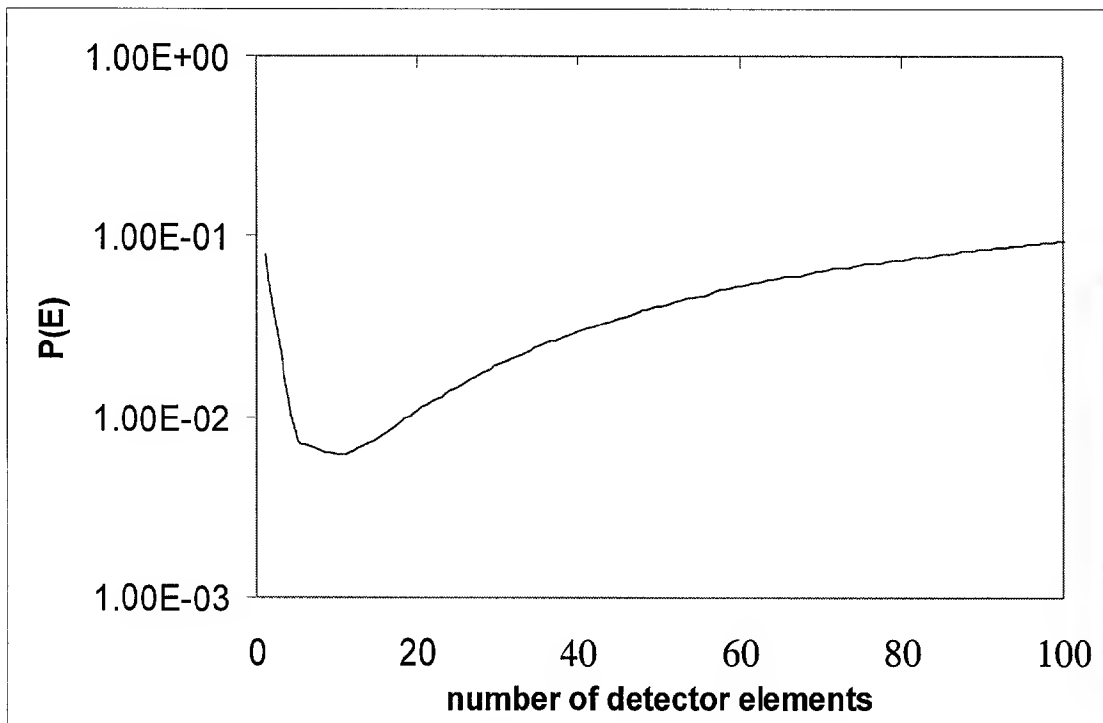


Fig. 9a

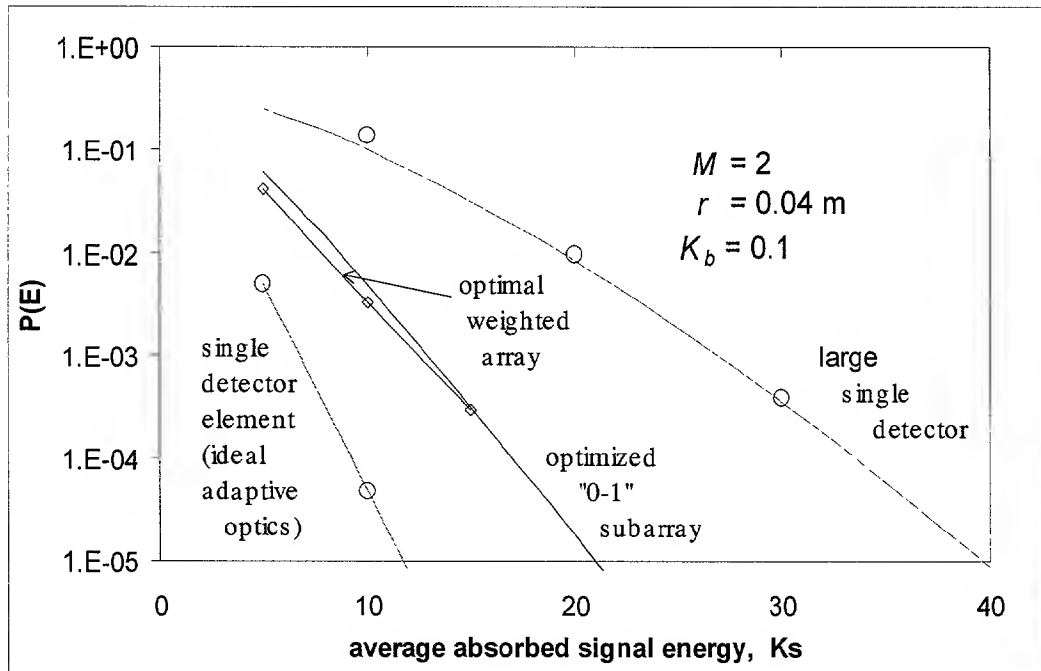


Fig. 9b

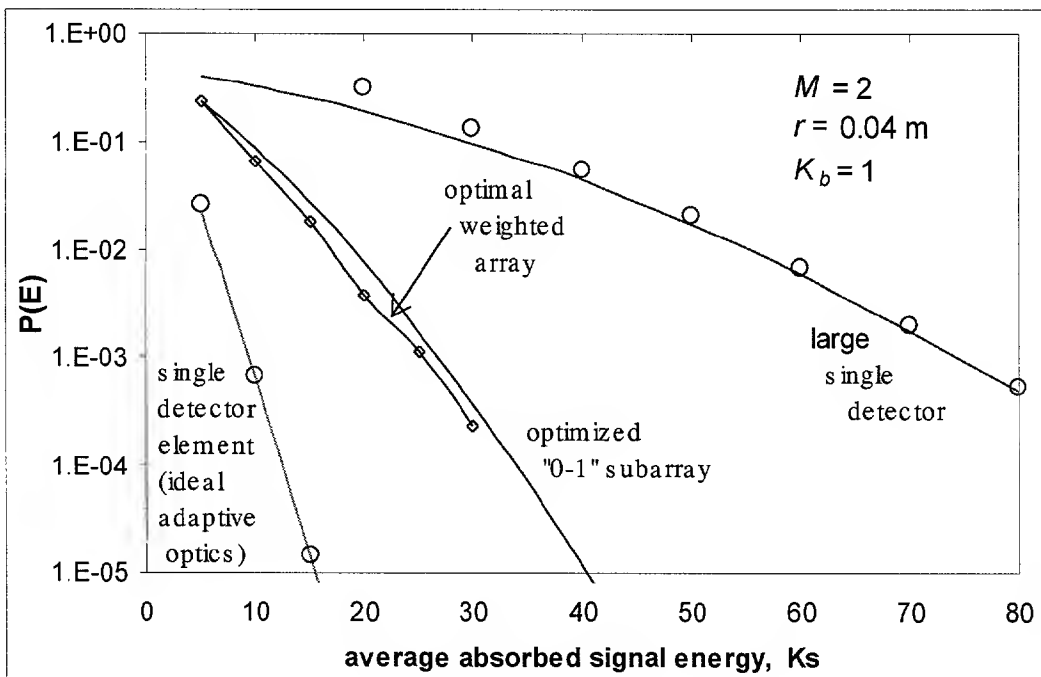


Fig. 9c

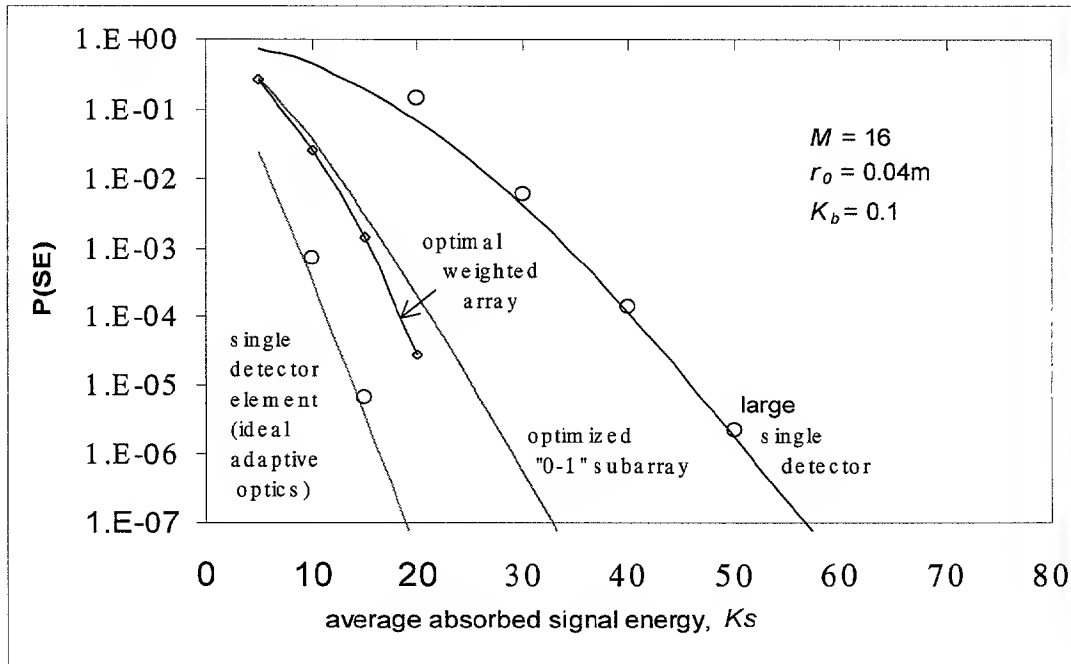


Fig. 9d

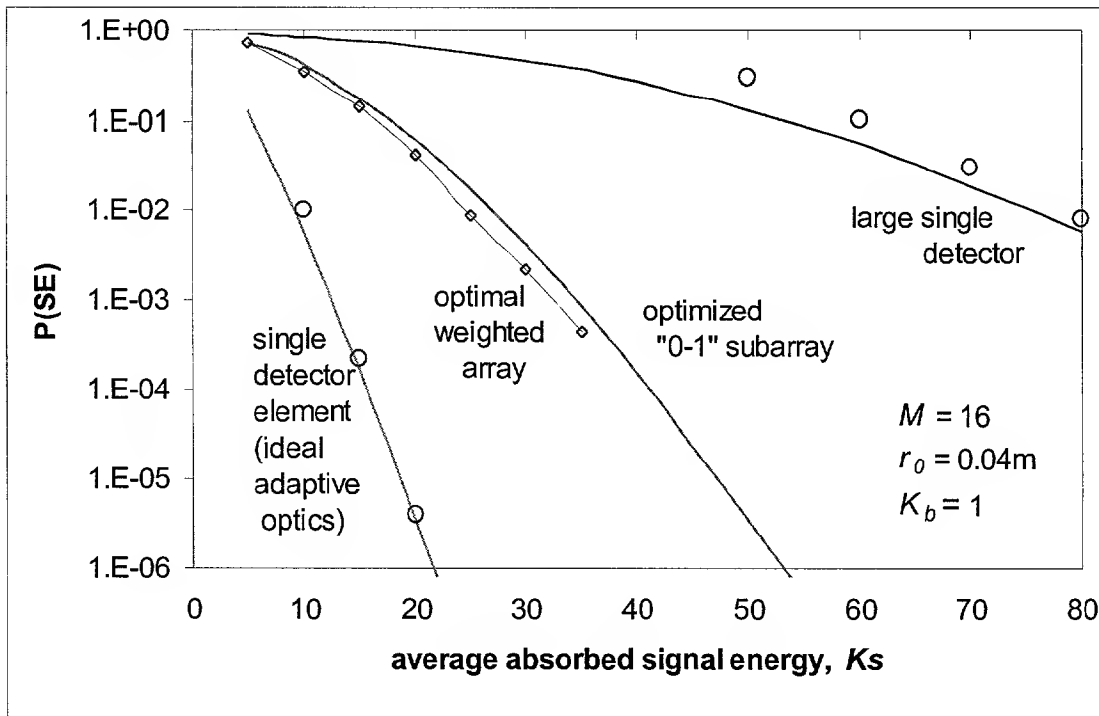


Fig. 10

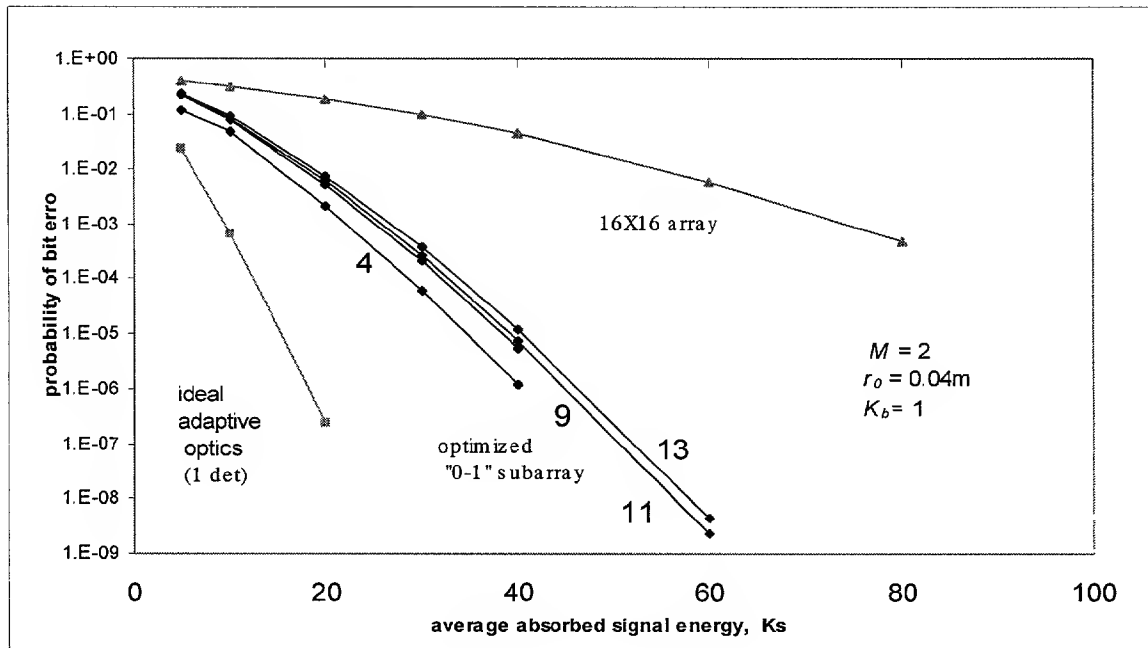


Fig. 11

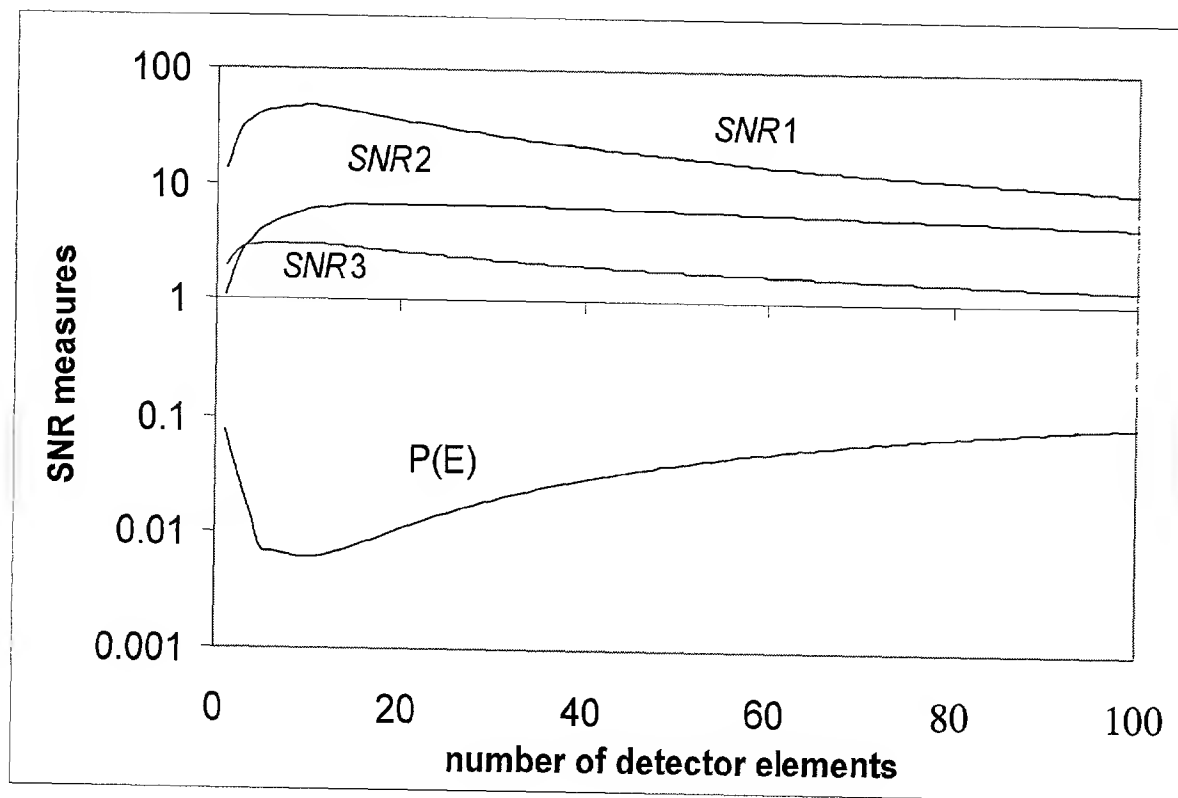


Fig. 12

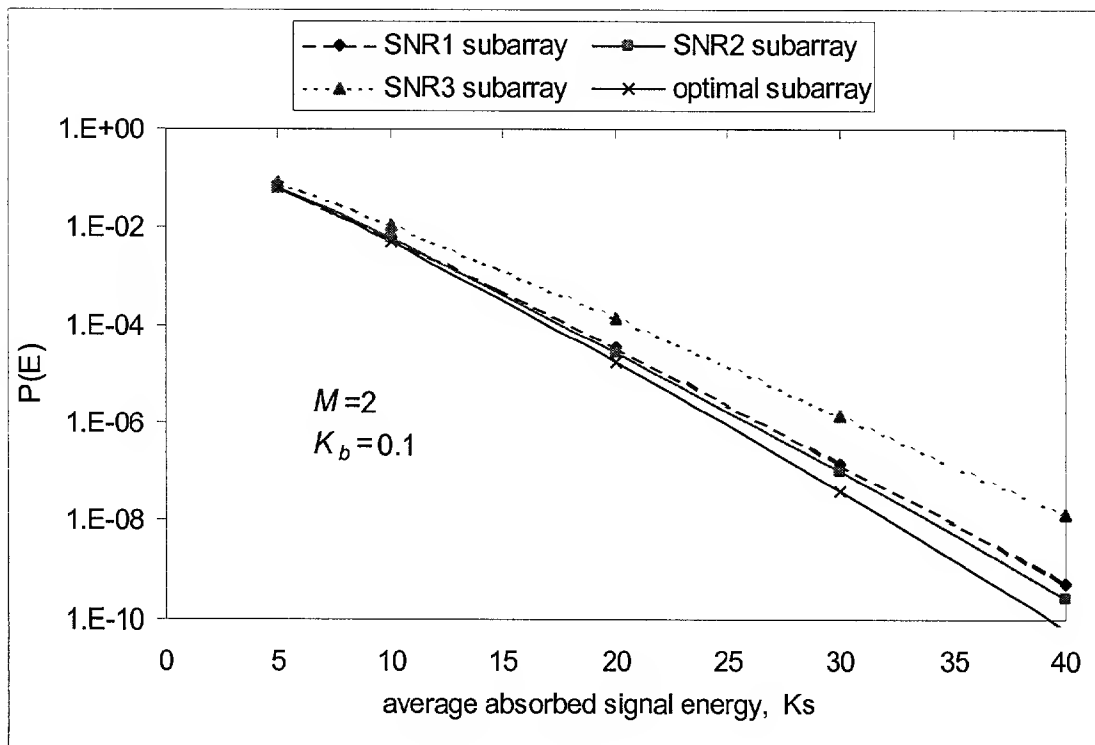


Fig. 13

